

Please add the following new claims:

sub 72
--45. The chip of claim 15 wherein the ESD positive lines are not connected to a steady voltage source.

46. The chip of claim 45 wherein each second diode has an anode electrically connected to a pad.

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47. The chip of claim 46 wherein a positive line is connected to the negative ring via a plurality of ESD switches.

48. The chip of claim 15 wherein each second diode has an anode electrically connected to a pad.

49. The chip of claim 48 wherein a positive line is connected to the negative ring via a plurality of ESD switches.

50. The chip of claim 15 wherein a positive line is connected to the negative ring via a plurality of ESD switches.--